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real-time applications. It is especially well-suited for readers looking to use SDRAM memories in systems with hard or firm real-time requirements. There is a strong focus on real-time concepts, such as predictability and composability, as well as a brief discussion about memory controller architectures for high-performance computing.

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Abstract. Background information on SDRAM technology is provided in Sect. 2.1.The properties of pattern-based memory controllers are introduced in Sect. 2.2.The story continues with a detailed description of our novel reconfigurable memory controller architecture in Sect. 2.3.In Sect. 2.4, we derive a worst-case performance model for this memory controller architecture, based on a Latency-rate ...

Reconfigurable Real-Time Memory Controller Architecture ...

Sergey Kolesnik has spent more than 20 years developing embedded systems and softwarefor a broad set of devices in industrial automation, smart cards,telecommunications, Internet systems, energy metering and more. Sergeygraduated from the Polytechnic University, Odessa, UKR with anEE-Electrical Drives and Industrial Automation. Sergey is currently themanager of the Microprogramming Department ...

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Real-Time Memory Controllers Yonghui Li 1, Benny Akesson 2 and Kees Goossens 1Eindhoven University of Technology, 2Czech Technical University in Prague Abstract—Memory controller design is challenging as real-time embedded systems feature an increasing diversity of real-time and non-real-time applications with variable transaction sizes.

Dynamic Command Scheduling for Real-Time Memory Controllers

As a first work toward this direction, we present two main contributions in this article: (1) a configurable real-time multichannel memory controller architecture with a novel method for logical-to-physical address translation and (2) two design-time methods to map memory clients to the memory channels, one an optimal algorithm based on an integer programming formulation of the mapping problem ...

A Real-Time Multichannel Memory Controller and Optimal ...

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